

EAST - [400.wsp:1]

File View Edit Tools Window Help

Drafts
Pending
Active
L1: (3189224) index\$3 or address\$3
L2: (1139200) revers\$3
L3: (13683) 1 with 2
L4: (71337) fft or fourier
L5: (198) 3 same 4
L6: (7211071) second ad1j (space\$1 bank\$1 memory)
L7: (369018) 1 with 6
L8: (165) 5 and 7
L9: (32) 1.ti. and 8
L10: (50) 1.ab. and 8 not 9
L11: (83) 8 not (9 10)
L12: (33) 5 not 8
L29: (3517) 708/4?? ccls.
L30: (48) 1.ti. and 29
L31: (23) 2 and 30
L32: (23) 6 and 31
L33: (25) 30 not 31
Failed
Saved
Favorites
Tagged (8)

Search
DBs: USPAT, US-PGPUB, EPO, JPO, DERWENT, BMSTDB
Default operator: OR
Highlight all hit terms initially

	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Cl	Inventor	S	C	P	3
1	<input type="checkbox"/>	<input type="checkbox"/>	US 6766433 B2	20040720	10	System having user programmable addressing mod	711/202	711/220, 714/702		Circello, Joseph C. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
2	<input type="checkbox"/>	<input type="checkbox"/>	US 6035313 A	20000307	26	Memory address generator for an FFT	708/404			Marchant, Jeffrey David	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
3	<input type="checkbox"/>	<input type="checkbox"/>	US 5491652 A	19960213	9	Fast Founier transform address generator	708/404			Luo, Wenzhe et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
4	<input type="checkbox"/>	<input type="checkbox"/>	US 3871577 A	19750318	12	Method and apparatus for addressing FFT processor	708/404			Avellar, Karl B. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
5	<input type="checkbox"/>	<input type="checkbox"/>	US 3673399 A	19720627	21	FFT PROCESSOR WITH UNIQUE ADDRESSING	708/404	708/408, 708/622		Hancke, Peter J. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
6	<input type="checkbox"/>	<input type="checkbox"/>	US 6115728 A	20000905	53	Fast founier transforming apparatus and method, variable	708/404			Nakai, Yuji et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
7	<input type="checkbox"/>	<input type="checkbox"/>	US H000570 H	19890103	11	Fast Fourier transform data address pre-scrambler circuit	708/200	708/403		Tylaska, Theodore T. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
8	<input type="checkbox"/>	<input type="checkbox"/>	US 6631167 B1	20031007	25	Process and device for	375/260	708/406, 708/408		Cambonie, Joel et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

Ready

NUM